

## EAST Search History

## EAST Search History (Prior Art)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	6	(pass\$3 trasmit\$4) near2 data with application with bios	USPAT	OR	OFF	2011/02/01 10:30
S2	5	(pass\$3 trasmit\$4) near2 data with application with bios	US-PGPUB	OR	OFF	2011/02/01 10:34
S3	0	(pass\$3 trasmit\$4) near2 data with application with bios	EPO; JPO; IBM_TDB	OR	OFF	2011/02/01 10:37
S4	0	data near3 transmit adj1 buffer near3 receive adj1 buffer	USPAT	OR	OFF	2011/02/01 11:23
S5	0	data near3 transmit adj1 buffer near3 receive adj1 buffer	US-PGPUB	OR	OFF	2011/02/01 11:23
S6	0	data near3 transmit adj1 buffer near3 receive adj1 buffer	EPO; JPO; IBM_TDB	OR	OFF	2011/02/01 11:23
S7	879	transmit adj1 buffer with receive adj1 buffer	USPAT	OR	OFF	2011/02/01 11:31
S8	405	transmit adj1 buffer with receive adj1 buffer with data	USPAT	OR	OFF	2011/02/01 11:31
S9	13	transmit adj1 buffer with receive adj1 buffer with data with connection	USPAT	OR	OFF	2011/02/01 11:31
S10	8	transmit adj1 buffer with receive adj1 buffer with data with connection	US-PGPUB	OR	OFF	2011/02/01 11:42
S11	1	transmit adj1 buffer with receive adj1 buffer with data with connection	EPO; JPO; IBM_TDB	OR	OFF	2011/02/01 11:43

S12	0	transmit adj1 buffer with receive adj1 buffer with pass\$3 with therebetween	USPAT	OR	OFF	2011/02/01 13:29
S13	1	transmit adj1 buffer with receive adj1 buffer with pass\$3 with therebetween	US-PGPUB	OR	OFF	2011/02/01 13:29
S14	0	transmit adj1 buffer with receive adj1 buffer with pass\$3 with therebetween	EPO; JPO; IBM_TDB	OR	OFF	2011/02/01 13:31
S15	62	buffer adj1 memory with transmit adj1 buffer with receive adj1 buffer	USPAT	OR	OFF	2011/02/01 13:34
S16	0	buffer adj1 memory with transmit adj1 buffer with receive adj1 buffer same bios	USPAT	OR	OFF	2011/02/01 13:35
S17	1	buffer adj1 memory with transmit adj1 buffer with receive adj1 buffer and bios	USPAT	OR	OFF	2011/02/01 13:35
S18	1	buffer adj1 memory with transmit adj1 buffer with receive adj1 buffer and bios	US-PGPUB	OR	OFF	2011/02/01 13:41
S19	0	buffer adj1 memory with transmit adj1 buffer with receive adj1 buffer and bios	EPO; JPO; IBM_TDB	OR	OFF	2011/02/01 13:41
S20	21	data with buffer with application same bios	USPAT	OR	OFF	2011/02/01 14:00
S21	13	data with buffer with application same bios	US-PGPUB	OR	OFF	2011/02/01 14:23
S22	2	data with buffer with application same bios	EPO; JPO; IBM_TDB	OR	OFF	2011/02/01 14:27
S23	2	bios near2 read\$3 near3 data with buffer	USPAT	OR	OFF	2011/02/01 14:41

S24	2	bios near2 read\$3 near3 data with buffer	US-PGPUB	OR	OFF	2011/02/01 14:45
S25	1	bios near2 read\$3 near3 data with buffer	EPO; JPO; IBM_TDB	OR	OFF	2011/02/01 14:45
S26	9	azuma.in. with keiichi	US-PGPUB; USPAT	OR	OFF	2011/02/01 14:56
S27	3	heitoh.in. with tsuneo	US-PGPUB; USPAT	OR	OFF	2011/02/01 15:00
S28	1	shibata.in. with eiichi	US-PGPUB; USPAT	OR	OFF	2011/02/01 15:14
S29	2877	713/1.ccls.	USPAT	OR	OFF	2011/02/01 15:29
S30	1289	713/1.ccls.	US-PGPUB	OR	OFF	2011/02/01 16:09
S31	0	713/1.ccls. and (pass \$3 trasmit\$4) near2 data with application with bios	US-PGPUB; USPAT	OR	OFF	2011/02/01 16:21
S32	0	713/1.ccls. and data near3 transmit adj1 buffer near3 receive adj1 buffer	US-PGPUB; USPAT	OR	OFF	2011/02/01 16:22
S33	0	713/1.ccls. and transmit adj1 buffer with receive adj1 buffer with data with connection	US-PGPUB; USPAT	OR	OFF	2011/02/01 16:22
S34	0	713/1.ccls. and transmit adj1 buffer with receive adj1 buffer with pass\$3 with therebetween	US-PGPUB; USPAT	OR	OFF	2011/02/01 16:22
S35	0	713/1.ccls. and buffer adj1 memory with transmit adj1 buffer with receive adj1 buffer and bios	US-PGPUB; USPAT	OR	OFF	2011/02/01 16:24
S36	1	713/1.ccls. and data with buffer with application same bios	US-PGPUB; USPAT	OR	OFF	2011/02/01 16:24
S37	0	713/1.ccls. and bios near2 read\$3 near3 data with buffer	US-PGPUB; USPAT	OR	OFF	2011/02/01 16:25

S38	2515	713/2.ccls.	USPAT	OR	OFF	2011/02/01 16:30
S39	1495	713/2.ccls.	US-PGPUB	OR	OFF	2011/02/01 16:53
S40	1	713/2.ccls. and (pass \$3 trasmit\$4) near2 data with application with bios	US-PGPUB; USPAT	OR	OFF	2011/02/01 17:24
S41	0	713/2.ccls. and data near3 transmit adj1 buffer near3 receive adj1 buffer	US-PGPUB; USPAT	OR	OFF	2011/02/01 17:24
S42	0	713/2.ccls. and transmit adj1 buffer with receive adj1 buffer with data with connection	US-PGPUB; USPAT	OR	OFF	2011/02/01 17:25
S43	0	713/2.ccls. and transmit adj1 buffer with receive adj1 buffer with pass\$3 with therebetween	US-PGPUB; USPAT	OR	OFF	2011/02/01 17:25
S44	0	713/2.ccls. and buffer adj1 memory with transmit adj1 buffer with receive adj1 buffer and bios	US-PGPUB; USPAT	OR	OFF	2011/02/01 17:25
S45	3	713/2.ccls. and data with buffer with application same bios	US-PGPUB; USPAT	OR	OFF	2011/02/01 17:25
S46	0	713/2.ccls. and bios near2 read\$3 near3 data with buffer	US-PGPUB; USPAT	OR	OFF	2011/02/01 17:25
S47	1821	713/100.ccls.	USPAT	OR	OFF	2011/02/01 17:32
S48	475	713/100.ccls.	US-PGPUB	OR	OFF	2011/02/01 18:01
S49	0	713/100.ccls. and (pass\$3 trasmit\$4) near2 data with application with bios	US-PGPUB; USPAT	OR	OFF	2011/02/01 18:15
S50	0	713/100.ccls. and data near3 transmit adj1 buffer near3 receive adj1 buffer	US-PGPUB; USPAT	OR	OFF	2011/02/01 18:15

S51	0	713/100.ccls. and transmit adj1 buffer with receive adj1 buffer with data with connection	US-PGPUB; USPAT	OR	OFF	2011/02/01 18:16
S52	0	713/100.ccls. and transmit adj1 buffer with receive adj1 buffer with pass\$3 with therebetween	US-PGPUB; USPAT	OR	OFF	2011/02/01 18:16
S53	0	713/100.ccls. and transmit adj1 buffer with receive adj1 buffer with pass\$3 with therebetween	US-PGPUB; USPAT	OR	OFF	2011/02/01 18:16
S54	0	713/100.ccls. and buffer adj1 memory with transmit adj1 buffer with receive adj1 buffer and bios	US-PGPUB; USPAT	OR	OFF	2011/02/01 18:16
S55	0	713/100.ccls. and data with buffer with application same bios	US-PGPUB; USPAT	OR	OFF	2011/02/01 18:17
S56	0	713/100.ccls. and bios near2 read\$3 near3 data with buffer	US-PGPUB; USPAT	OR	OFF	2011/02/01 18:17

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